PTO/SB/21 (09-04)

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## TRANSMITTAL **FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	10/775,310
Filing Date	February 10, 2004
First Named Inventor	FIEDLER, Alan
Art Unit	30,500-05US
Examiner Name	Ton, My Trang
Attorney Docket Number	30.500-05US

ENCLOSURES (Check all that apply)								
	Fee Tran	smittal Form	V	Drawing(s)			After Allowance Communication to TC	
	F	ee Attached		Licensing-related Papers			Appeal Communication to Board of Appeals and Interferences	
	Extension Express A Information Certified Cocument Reply to N	fter Final  ffidavits/declaration(s)  n of Time Request  Abandonment Request  on Disclosure Statement  Copy of Priority	Petition Petition to Convert to a Provisional Application Power of Attorney, Re Change of Correspond Terminal Disclaimer Request for Refund CD, Number of CD(s) Landscape Table Remarks PTOL-85 (1 pg) Submission of Formal Drawing Check: \$1,000.00		e Address		Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)  Proprietary Information  Status Letter  Other Enclosure(s) (please Identify below):	
	Reply to Missing Parts under 37 CFR 1.52 or 1.53		Return receipt postcard					
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT								
Firm Name Fulbright & Jaworski L.L.P.								
Signati	ature Oou/Co							
Printed	name	John F. Klos, Esq.						
Date		December 20, 2005			Reg. No.	37,162		

## CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature John F. Klos, Esq. Typed or printed name

Date December 20, 2005

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: FIEDLER, Alan	)	PATENT
Serial No.: 10/775,310	)	Art Group Unit: 2816
Filed: February 10, 2004	)	Examiner: Ton, My Trang
Title: Phase Multiplier Circuit	)	Atty. Docket No.: 30,500-05US

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **SUBMISSION OF FORMAL DRAWINGS**

Applicant hereby submits the formal drawings for the above-referenced application and requests that these drawings be accepted for filing.

By:

Please direct any questions or comments regarding this application to John F. Klos at 612-321-2806.

Alan Fiedler by his attorneys,

Dated: December 20, 2005

John F. Klos, Esq., Registration No. 37,162

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